

Sujit Prakash Gujar

CONTACT INFORMATION

E - Commerce Laboratory,
Department of Computer Science and Automation,
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RESEARCH INTERESTS

Game Theory and Mechanism Design, Mechanism Design for Heterogeneous Settings, Optimal Mechanism Design, Redistribution Mechanisms, Dynamic Mechanism Design, Multi-Agent Systems. Algorithmic Game Theory, Algorithmic Mechanism Design. Mechanism Design for Carbon Economics. Cryptographic Game Theory. (Privacy in Auctions) Cryptography, Information Security.

EDUCATION

P.hD. [2006 - Current]
Department of Computer Science and Automation (CSA),
Indian Institute of Science (IISc), Bangalore, India.
CGPA: 6.9/8.0.

M.E.(ISE.), [2004 - 2006]
Department of Computer Science and Automation (CSA),
Indian Institute of Science (IISc), Bangalore, India.
Thesis Title: Measures for Classification and Detection in Steganalysis
Thesis Supervisor: Prof. C E Veni Madhavan.
CGPA: 7.2/8.0 **Rank 1**

B.E. (ECE), [1997 - 2001],
Govt. College of Engineering, Pune
Score: 70.00%

HSC, [1995-1997],
Maharashtra State Board of Secondary and Higher Secondary Education.
Score: 90.00%

SUMMER INTERNSHIP

Prof David C Parkes. Harvard EconCS Group, School of Engineering and Applied Sciences, Harvard University. (Summer 2009.)

PUBLICATIONS

Journal Publications

Dinesh Garg, Y. Narahari, Sujit Gujar, “*Foundations of Mechanism Design: A Tutorial - Part 1: Key Concepts and Classical Results.*” *Sadhana - Indian Academy Proceedings in Engineering Sciences*, Volume 33, Part 2, pp 83-130, April 2008.

Dinesh Garg, Y. Narahari, Sujit Gujar, “*Foundations of Mechanism Design: A Tutorial - Part 2: Advanced Concepts and Results.*” *Sadhana - Indian Academy Proceedings in Engineering Sciences*, Volume 33, Part 2,ssss pp 131-174, April 2008.

Book Chapters

Y. Narahari and Sujit Gujar, “*Auctions in Electronic Commerce*”. Invited Book Chapter in: *The Handbook of Technology Management*, John Wiley and Sons, To appear in 2009.

Conference Publications

Sujit Gujar and Y. Narahari, “*Optimal Multi-Unit Combinatorial Auctions with Single Minded Bidders*”. The 11th IEEE Conference on Commerce and Enterprise Computing (CEC’09), *To appear*.

Sujit Gujar and Y Narahari, “*Redistribution of VCG Payments in Assignment of Heterogeneous Objects*”. In *Proceedings of the 4th International Workshop on Internet and Network Economics (WINE)*, 2008, pp 438-445.

Sujit Gujar and Y Narahari, “*An Optimal Multi-Unit Combinatorial Procurement Auction with Single Minded Bidders*”. *Managing Complexity in a Distributed World, a Centenary Conference of Division of Electrical Sciences, Indian Institute of Science, (MCDES) 2008.*

Sujit P Gujar and C E Veni Madhavan, “*Measures for Classification and Detection in Steganalysis*”, In *Proceedings of 3rd Workshop on Computer Vision, Graphics and Image Processing- WCVGIP 2006*, pp. 210-214, January 2006.

Working Papers

Sujit Gujar and Y. Narahari, “*Redistribution Mechanisms for Assignment of Heterogeneous Objects*”. Working Paper, Electronic Commerce Laboratory, Department

COURSE WORK AT IISc

ME Courses (Grade)		PhD Courses (Grade)
Cryptography (S)	Game Theory(S)	Analysis-I (S)
Network Storage and Security.(S)	Design and Analysis of Algorithms(A)	Topology(A)
Data Structures(B)	E-commerce(S)	Linear Algebra(A)
Computational Methods of Optimization(A)	Computer Communication Network (A)	Topics in Approximation Algorithms(B)
Discrete Structures(A)	Pattern Recognition(B)	

Other Courses (Audit)

Stochastic Modeling and Applications, Topology-II, Discrete Structures, Analysis-II (Measure Theory), Topics in Graph Theory, Algebra and Computation, Modeling and Algorithms for large data sets.

ACADEMIC HONORS

Recipient of Infosys Fellowship.

Rank 1, ME ISE, IISc, 2004-2006 batch.

GATE Score **99.66** percentile, MHRD Scholarship holder.

Stood 5th in '**Ganit Parangant**' state level (Maharashtra) examination. Ganit Paragat is Mathematical examination conducted at school level for testing mathematical abilities.

Selected through **IIT JEE** (Joint Entrance Examination) for admission to IIT. (AIR2255).

SKILLS

Proficient in Programming for multiprocessor environment.

Programming Languages: C, C++, MFC, Java.

Tools: Matlab, VC++, Netbeans, Eclipse.

INDUSTRY EXPERIENCE

I worked as Software Engineer in PACE Soft Silicon Pvt. Ltd. (July 2001 - July 2004). During this period I acquainted various skills such as programming for multiprocessor environment, cryptography, network security and multimedia related technologies. I got exposure to work with various embedded OS on different platforms.

REFERENCES

Prof. Y. Narahari,
Department of Computer Science and Automation,
Indian Institute of Science Bangalore, India.

Prof. C E Veni Madhavan,
Department of Computer Science and Automation,
Indian Institute of Science Bangalore, India.

Prof. M. Narasimha Murty,
Department of Computer Science and Automation,
Indian Institute of Science Bangalore, India.
